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REQUEST

FOR

CONTINUED EXAMINATION (RCE) TRANSMITTAL

Address to: **Commissioner for Patents Box RCE** Washington, DC 20231

TO THE OTHER DESIGNATION OF THE PERSON OF TH			
Application Number	09/885,792		
Filing Date	6/20/2001		
First Named Inventor	Jagannathan et al.		
Art Unit	2812		
Examiner Name	Christopher W. Lattin		
Attorney Docket Number	FIS920000402US1_		

Paguest for Continued Ex	Continued Examination (RCE) und amination (RCE) practice under 37 CFR application. See Instruction Sheet for RC	1,114 does not apply to a	any utility or plant application	ou usectanoi-tozanie	
a. Previously su i. Consider the (Any unenter ii. Consider the iii. Other b. Enclosed i. Amend ii. Affidav 2. Miscellaneous a period of b. Other 3. Fees The RCE a. The Director Deposit Acco i. RCE fe ii. Extens iii. Other b. Check in the c. Payment by WARNING: I	amendment(s)/reply under 37 CFR §1.1 ed amendment(s) referred to above will be arguments in the Appeal Brief ment/Reply iii.	or Reply Brief previous for Reply Brief previous formation Disclosure of the provious formation Disclosure of the provious formation is requested a month; Fig. 37 CFR §1.114 when the following fees, or the provious formation of the provious formation o	re Statement (IDS) sted under 37 CFR §1 see under 37 CFR§1.17(i) in the RCE is filed. credit any overpayment	required) ents, to	4FD
<u> </u>	SIGNATURE OF APPLICANT,			48,757	\dashv
Name (Print / Type) Signature	Olga V. Merkoulova	P Date	O3 10, C		ナ
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TRANSMITTAL LETTER (General - Patent Pending)

Docket No. FIS920000402US1

Jagannathan et al.

Serial No.

Filing Date

Examiner

Group Art Unit

09/885,792

6/20/2001

Christopher W. Lattin

2812

Title: A NON-SELF-ALIGNED SI/GE HETEROJUNCTION BIPOLAR TRANSISTOR

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is:

RCE (Request for Continued Examination)

Amendment 1:116

in the above identified application.

- No additional fee is required.
- A check in the amount of

is attached.

The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. as described below. A duplicate copy of this sheet is enclosed.

09-0458

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Dated:

03, 10,03

Olga V. Merkoulova Registration No. 48,757 Whitham, Curtis, & Christofferson, P.C. 11491 Sunset Hills Road, Suite 340 Reston, Virginia 20190 703-787-9400

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In re patent application of

Docket No. FIS920000402US1

Basanth Jagannathan

Serial No.:09/885,792

Filed: June 20, 2001

Group Art Unit: No.2812

Examiner: Christopher W. Lattin

For: A NON-SELF-ALIGNED SI/GE HETEROJUNCTION BIPOLAR

TRANSISTOR

Box: Final amendment

Assistant Commissioner of Patents and Trademarks

Washington, D.C. 20231

BOX "AF" RESPONSE UNDER 37 C.F.R. § 1.116. --- EXPEDITE PROCEDURE---EXAMINING GROUP 28/2

Sir:

nal amendment
at Commissioner of Patents and Trademarks
gton, D.C. 20231

AMENDMENT UNDER 37 C.F.R. §1.116

In response to the Office Action dated December 11, 2002, please amend the above.

The page 12.15.

Th identified application as follows:

IN THE SPECIFICATION:

A marked-up copy of the changes to selected paragraphs is provided below. A clean copy of these changes is provided in the attached separate sheet, entitled "Clean Version of Changes to Specification". Please replace the marked-up paragraphs with the replacement clean copy of the selected paragraphs attached hereto.

Marked up paragraph at page 7, lines 1-7.

It is another object of the present invention to provide a method for making a heterojunction bipolar transistor which performs low termal-cycle processing, which, in turn, allows the present method to use thin low-temperature epitaxy (LTE) layers in the formation of base and collector regions. Use of thin LTE layers for these regions increases speed of the transistor and, further, leads to a lowering of the overall topography of the device, making midend-of-line (MEOL) processed such as emitter, base, and collector contact opening much easier.